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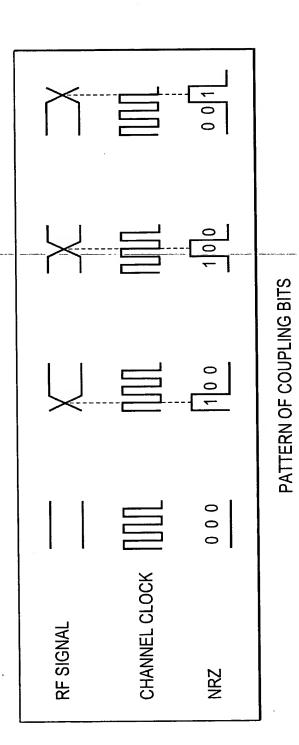


FIG.3

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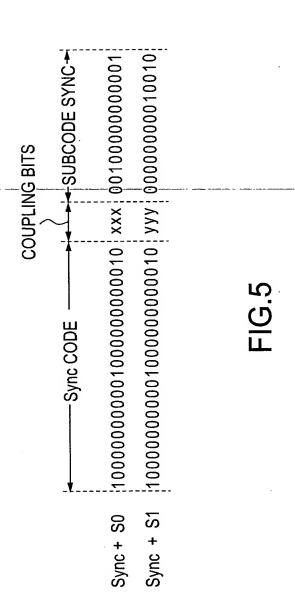
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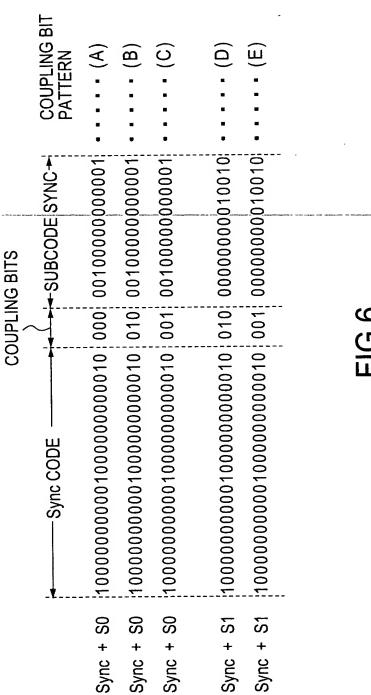
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FRAME	1				S					★ 山
	2				S	1				₹
	3	P1	Q1	R1	S1	T 1	U1	V1	W1	35
	4	P2	Q2	R2.	S2	T2	U2	V2	W2	S
	5	P3	Q3	R3	S3	T3	U3	V3	W3	ONE SUBCODING FRAME
										<u>`</u>
					•					SU
	96	P94	Q94	R94	S94	T94	U94	V94	W94	当
	97	P95	Q95	R95	S95	T95	U95	V95	W95	δ O
	98	P96	Q96	R96	S96	T96	U96	V96	W96	
	1				S	0				
	2				S	1				
	3	P1	Q1	R1	S1	T1	U1	V1	W1	
						1				
					•	ı				
		i								ı

S0 = 001000000000001S1 = 000000000010010

FIG.4





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S0/S1	COUPLING BIT PATTERN	CONTENTS
S0[0]	Α	S(Sync)
S1[0]	D,E=(0,1)	P(Parity)
S0[1]	B,C=(0,1)	K[7] (MSB)
S1[1]	D,E=(0,1)	K[6]
S0[2]	B,C=(0,1)	K[5]
S1[2]	D,E=(0,1)	K[4]
S0[3]	B,C=(0,1)	K[3]
S1[3]	D,E=(0,1)	K[2]
S0[4]	B,C=(0,1)	K[1]
S1[4]	D,E=(0,1)	K[0] (LSB)

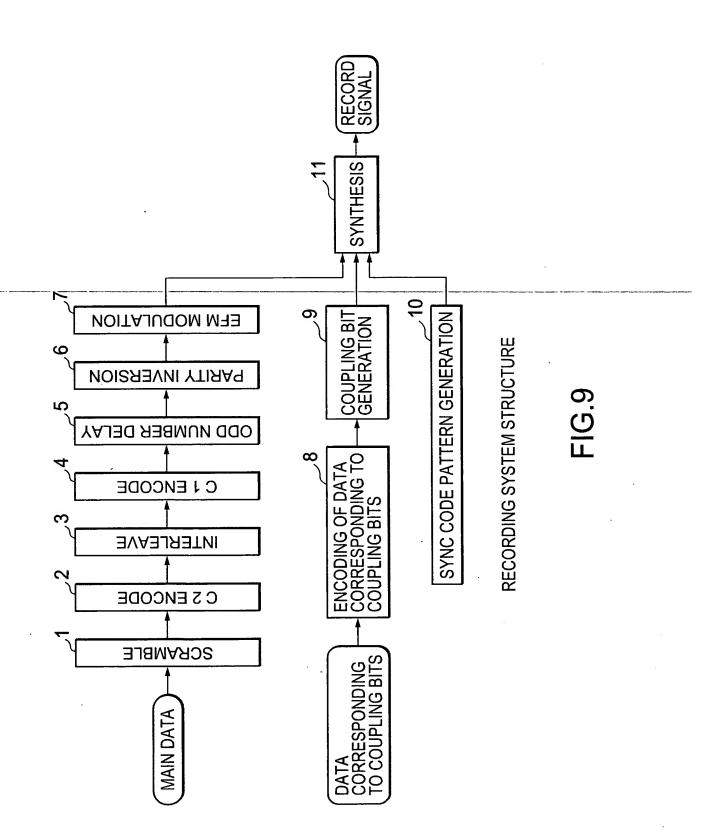
FIG.7

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S0/S1	COUPLING BIT PATTERN	CONTENTS
S0[0]	Α	S(Sync)
S1[0]	D,E=(0,1)	P(Parity)
S0[1]	B,C=(0,1)	K[7] (MSB)
S1[1]	D,E=(0,1)	K:inv[7](INVERSION BIT)
S0[2]	B,C=(0,1)	K[6]
S1[2]	D,E=(0,1)	K:inv[6]
S0[3]	B,C=(0,1)	K[5]
S1[3]	D,E=(0,1)	K:inv[5]
S0[4]	B,C=(0,1)	K[4]
S1[4]	D,E=(0,1)	K:inv[4]
S0[5]	B,C=(0,1)	K[3]
S1[5]	D,E=(0,1)	K:inv[3]
S0[6]	B,C=(0,1)	K[2]
S1[6]	D,E=(0,1)	K:inv[2]
S0[7]	B,C=(0,1)	K[1]
S1[7]	D,E=(0,1)	K:inv[1]
S0[8]	B,C=(0,1)	K[0](LSB)
S1[8]	D,E=(0,1)	K:inv[0]

FIG.8



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